

C8051F020 TIMERS RELATED DATA

frequency		16 - bit counter	13 - bit counter	8 - bit counter
f=22.1134 MHz	period	0.0452 usec	0.0452 usec	0.0452 usec
	count/overflow	65536	8192	256
	time/overflow	2.963 msec	0.3704 msec	11.57 usec
	overflows/sec	338	2700	86400
f/12	period	0.543 usec	0.543 usec	0.543 usec
	count/overflow	65536	8192	256
	time/overflow	35.556 msec	4.4444 msec	138.9 usec
	overflows/sec	28	225	7200

Timer 0 and Timer 1 Registers

Clock Control Register								Timer Mode Register								Timer Control Register							
CKCON								TMOD								TCON							
Unused	T4M	T2M	T1M	T0M	RESERVED	RESERVED	RESERVED	GATE1	C/T1	T1M1	T1M0	GATE0	C/T0	T0M1	T0M0	TF1	TR1	TF0	TR0	IE1	IT1	IE0	IT0
7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0	7	6	5	4	3	2	1	0

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Each register is bit addressable

IT: Interrupt Type select

TF: Timer overFlow

TR: TimeR enable

IE: Interrupt (External)

Timer 0 High Byte Timer 0 Low Byte



TH0



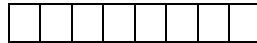
TL0

Timer 0 Status Registers

Timer 1 High Byte Timer 1 Low Byte



TH1

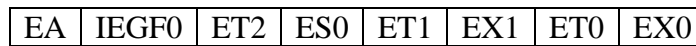


TL1

Timer 1 Status Registers

IE

Interrupt Enable Register



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